

**PL123-05SI-R Information**


For Reference Only

**Part Number** [PL123-05SI-R](#)  
**Manufacturer** Microchip Technology  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC CLK MULTPLX 1:5 134MHZ 8SOIC  
**Package** 8-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**PL123-05SI-R Specifications**

Manufacturer Part Number	<a href="#">PL123-05SI-R</a>
Manufacturer	Microchip Technology
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	8-SOIC (0.154", 3.90mm Width)
Series	-
Type	-
PLL	Yes with Bypass
Input	Clock
Output	Clock
Number of Circuits	1
Ratio - Input:Output	1:5
Differential - Input:Output	No/No
Frequency - Max	134MHz
Divider/Multiplier	No/No
Voltage - Supply	3 V ~ 3.6 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOP

[Report errors?](#)

## PL123-05SI-R Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## PL123-05SI-R Payment Methods



## PL123-05SI-R Shipping Methods



If you have any question about PL123-05SI-R, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)