

# PL685-P8-068OC

## PL685-P8-068OC Information

	Part Number	PL685-P8-068OC	
Sow helsener.com	Manufacturer	Microchip Technology	
	Category	Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
	Description	IC CLK BUFFER LVPECL 16TSSOP	- 50 - 52
	Package	16-TSSOP (0.173", 4.40mm Width)	回路装装
For Reference Only		For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



# PL685-P8-068OC Specifications

Manufacturer Part Number	PL685-P8-068OC		
Manufacturer	Microchip Technology		
Category	Integrated Circuits (ICs)		
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers		
Package	16-TSSOP (0.173", 4.40mm Width)		
Series	-		
Туре	-		
PLL	Yes with Bypass		
Input	LVPECL, Crystal		
Output	PECL		
Number of Circuits	1		
Ratio - Input:Output	1:2		
Differential - Input:Output	No/No		
Frequency - Max	225MHz		
Divider/Multiplier	Yes/Yes		
Voltage - Supply	2.97 V ~ 3.63 V		
Operating Temperature	$0^{\circ}$ C ~ $70^{\circ}$ C		
Mounting Type	Surface Mount		
Package / Case	16-TSSOP (0.173", 4.40mm Width)		
Supplier Device Package	16-TSSOP		
	Report errors		

#### PL685-P8-068OC Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE EUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

## PL685-P8-068OC Payment Methods



# PL685-P8-068OC Shipping Methods



If you have any question about PL685-P8-068OC, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com