



PMDPB65UP,115 Information



For Reference Only

Part Number PMDPB65UP,115

Manufacturer NXP

Category Discrete Semiconductor Products

Transistors - FETs, MOSFETs - Arrays

Description MOSFET 2P-CH 20V 3.5A SOT1118

Package 6-UDFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









PMDPB65UP,115 Specifications

Manufacturer Part Number	PMDPB65UP,115
Manufacturer	NXP
Category	Discrete Semiconductor Products
	Transistors - FETs, MOSFETs - Arrays
Package	6-UDFN Exposed Pad
Series	-
FET Type	2 P-Channel (Dual)
FET Feature	Logic Level Gate
Drain to Source Voltage (Vdss)	20V
Current - Continuous Drain (Id) @ 25°C	3.5A
Rds On (Max) @ Id, Vgs	70 mOhm @ 1A, 4.5V
Vgs(th) (Max) @ Id	1V @ 250μA
Gate Charge (Qg) (Max) @ Vgs	6nC @ 4.5V
Input Capacitance (Ciss) (Max) @ Vds	380pF @ 10V
Power - Max	520mW
Operating Temperature	150°C (TJ)
Mounting Type	Surface Mount
Package / Case	6-UDFN Exposed Pad
Supplier Device Package	DFN2020-6
	Report errors?

PMDPB65UP,115 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

PMDPB65UP,115 Payment Methods



















PMDPB65UP,115 Shipping Methods













If you have any question about PMDPB65UP,115, please do not hesitate to contact us!

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