

**S912ZVHL64F1VLL Information**


For Reference Only

**Part Number** [S912ZVHL64F1VLL](#)  
**Manufacturer** NXP  
**Category** Integrated Circuits (ICs)  
[Embedded - Microcontrollers](#)  
**Description** MAGNIV 16-BIT MCU S12Z CORE 64  
**Package** 100-LQFP  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**S912ZVHL64F1VLL Specifications**

Manufacturer Part Number	<a href="#">S912ZVHL64F1VLL</a>
Manufacturer	NXP
Category	Integrated Circuits (ICs) <a href="#">Embedded - Microcontrollers</a>
Package	100-LQFP
Series	S12 MagniV
Core Processor	S12Z
Core Size	16-Bit
Speed	32MHz
Connectivity	CANbus, I <sup>2</sup> C, LINbus, SCI, SPI
Peripherals	DMA, LCD, POR, PWM, WDT
Number of I/O	73
Program Memory Size	64KB (64K x 8)
Program Memory Type	FLASH
EEPROM Size	2K x 8
RAM Size	4K x 8
Voltage - Supply (Vcc/Vdd)	4.5 V ~ 5.5 V
Data Converters	A/D 4x10b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 105°C (TA)
Mounting Type	-
Package / Case	100-LQFP
Supplier Device Package	100-LQFP (14x14)

[Report errors?](#)

## S912ZVHL64F1VLL Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## S912ZVHL64F1VLL Payment Methods



## S912ZVHL64F1VLL Shipping Methods



If you have any question about S912ZVHL64F1VLL, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)