

**S912ZVML32F3VKH Information**


For Reference Only

**Part Number** [S912ZVML32F3VKH](#)  
**Manufacturer** NXP  
**Category** Integrated Circuits (ICs)  
[Embedded - Microcontrollers](#)  
**Description** MAGNIV 16-BIT MCU S12Z CORE 32  
**Package** 64-LQFP Exposed Pad  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**S912ZVML32F3VKH Specifications**

Manufacturer Part Number	<a href="#">S912ZVML32F3VKH</a>
Manufacturer	NXP
Category	Integrated Circuits (ICs) <a href="#">Embedded - Microcontrollers</a>
Package	64-LQFP Exposed Pad
Series	S12 MagniV
Core Processor	S12Z
Core Size	16-Bit
Speed	50MHz
Connectivity	CANbus, LINbus, SCI, SPI
Peripherals	DMA, POR, PWM, WDT
Number of I/O	31
Program Memory Size	32KB (32K x 8)
Program Memory Type	FLASH
EEPROM Size	512 x 8
RAM Size	4K x 8
Voltage - Supply (Vcc/Vdd)	3.5 V ~ 40 V
Data Converters	A/D 9x12b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 105°C (TA)
Mounting Type	-
Package / Case	64-LQFP Exposed Pad
Supplier Device Package	64-LQFP-EP (10x10)

[Report errors?](#)

## S912ZVML32F3VKH Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## S912ZVML32F3VKH Payment Methods



## S912ZVML32F3VKH Shipping Methods



If you have any question about S912ZVML32F3VKH, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)