

SI5319A-C-GMR Information


For Reference Only

Part Number [SI5319A-C-GMR](#)
Manufacturer Silicon Labs
Category Integrated Circuits (ICs)
[Clock/Timing - Application Specific](#)
Description IC CLOCK MULT/ATTENUATOR 36QFN
Package 36-VFQFN Exposed Pad
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com



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Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


SI5319A-C-GMR Specifications

| | |
|-----------------------------|--|
| Manufacturer Part Number | SI5319A-C-GMR |
| Manufacturer | Silicon Labs |
| Category | Integrated Circuits (ICs) Clock/Timing - Application Specific |
| Package | 36-VFQFN Exposed Pad |
| Series | DSPLL? |
| PLL | Yes |
| Main Purpose | Ethernet, SONET/SDH |
| Input | Clock, Crystal |
| Output | CML, CMOS, LVDS, LVPECL |
| Number of Circuits | 1 |
| Ratio - Input:Output | 1:1 |
| Differential - Input:Output | Yes/Yes |
| Frequency - Max | 1.4GHz |
| Voltage - Supply | 1.71 V ~ 3.63 V |
| Operating Temperature | -40°C ~ 85°C |
| Mounting Type | Surface Mount |
| Package / Case | 36-VFQFN Exposed Pad |
| Supplier Device Package | 36-QFN (6x6) |

[Report errors?](#)

SI5319A-C-GMR Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

SI5319A-C-GMR Payment Methods



SI5319A-C-GMR Shipping Methods



If you have any question about SI5319A-C-GMR, please do not hesitate to contact us!

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