

**SI5325C-C-GM Information**


For Reference Only

**Part Number** [SI5325C-C-GM](#)  
**Manufacturer** Silicon Labs  
**Category** Integrated Circuits (ICs)  
 Clock/Timing - Clock Generators, PLLs,  
 Frequency Synthesizers  
**Description** IC UP-PROG CLK MULTIPLIER 36-QFN  
**Package** 36-VFQFN Exposed Pad  
 For the pricing/inventory/lead time, please contact  
 us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**SI5325C-C-GM Specifications**

Manufacturer Part Number	<a href="#">SI5325C-C-GM</a>
Manufacturer	Silicon Labs
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	36-VFQFN Exposed Pad
Series	DSPLL?
Type	Clock Multiplier
PLL	Yes
Input	Clock
Output	CML, LVCMOS, LVDS, LVPECL
Number of Circuits	1
Ratio - Input:Output	2:2
Differential - Input:Output	Yes/Yes
Frequency - Max	346MHz
Divider/Multiplier	Yes/Yes
Voltage - Supply	1.71 V ~ 3.63 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	36-VFQFN Exposed Pad
Supplier Device Package	36-QFN (6x6)

[Report errors?](#)

## SI5325C-C-GM Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SI5325C-C-GM Payment Methods



## SI5325C-C-GM Shipping Methods



If you have any question about SI5325C-C-GM, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)