

SI5332H-D-GM3

SI5332H-D-GM3 Information

umber SI5332H-D-GM3	
acturer Silicon Labs	ET SATA ET
ry Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers	
ption BASE/BLANK PROTOTYPING DEVICE	
ge 48-TFQFN Exposed Pad	i i na hú de la
	E101#724
Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote
	acturer Silicon Labs ry Integrated Circuits (ICs) Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers ption BASE/BLANK PROTOTYPING DEVICE ge 48-TFQFN Exposed Pad For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



SI5332H-D-GM3 Specifications

Manufacturer Part Number	SI5332H-D-GM3
Manufacturer	Silicon Labs
Category	Integrated Circuits (ICs)
	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
Package	48-TFQFN Exposed Pad
Series	MultiSynth TM
Туре	Clock Generator
PLL	Yes
Input	LVCMOS, Crystal
Output	HCSL, LVCMOS, LVDS, LVPECL
Number of Circuits	1
Ratio - Input:Output	3:12
Differential - Input:Output	Yes/Yes
Frequency - Max	200MHz
Divider/Multiplier	Yes/No
Voltage - Supply	1.71 V ~ 3.63 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	48-TFQFN Exposed Pad
Supplier Device Package	48-QFN (6x6)
	Report error

SI5332H-D-GM3 Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

SI5332H-D-GM3 Payment Methods





If you have any question about SI5332H-D-GM3, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com