

**SI5342D-B-GM Information**


For Reference Only

**Part Number** [SI5342D-B-GM](#)  
**Manufacturer** Silicon Labs  
**Category** Integrated Circuits (ICs)  
[Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers](#)  
**Description** IC CLK BUFFER PLL 44QFN  
**Package** 44-VFQFN Exposed Pad  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**SI5342D-B-GM Specifications**

Manufacturer Part Number	<a href="#">SI5342D-B-GM</a>
Manufacturer	Silicon Labs
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	44-VFQFN Exposed Pad
Series	-
Type	-
PLL	Yes
Input	LVC MOS, LVDS, LVPECL, Crystal
Output	CML, HCSL, LVC MOS, LVDS, LVPECL
Number of Circuits	1
Ratio - Input:Output	5:2
Differential - Input:Output	Yes/Yes
Frequency - Max	350MHz
Divider/Multiplier	Yes/No
Voltage - Supply	1.71 V ~ 3.47 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	44-VFQFN Exposed Pad
Supplier Device Package	44-QFN (7x7)

[Report errors?](#)

## SI5342D-B-GM Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SI5342D-B-GM Payment Methods



## SI5342D-B-GM Shipping Methods



If you have any question about SI5342D-B-GM, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)