

**SI5351B-A-GU Information**


For Reference Only

**Part Number** [SI5351B-A-GU](#)  
**Manufacturer** Silicon Labs  
**Category** Integrated Circuits (ICs)  
 Clock/Timing - Clock Generators, PLLs,  
 Frequency Synthesizers  
**Description** IC CLK GENERATOR 200MHZ 24QSOP  
**Package** 24-SSOP (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact  
 us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**SI5351B-A-GU Specifications**

Manufacturer Part Number	<a href="#">SI5351B-A-GU</a>
Manufacturer	Silicon Labs
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	24-SSOP (0.154", 3.90mm Width)
Series	MultiSynth?
Type	Clock Generator
PLL	Yes
Input	Crystal
Output	LVC MOS
Number of Circuits	1
Ratio - Input:Output	1:8
Differential - Input:Output	No/No
Frequency - Max	200MHz
Divider/Multiplier	Yes/No
Voltage - Supply	1.71 V ~ 3.6 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	24-SSOP (0.154", 3.90mm Width)
Supplier Device Package	24-QSOP

[Report errors?](#)

## SI5351B-A-GU Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SI5351B-A-GU Payment Methods



## SI5351B-A-GU Shipping Methods



If you have any question about SI5351B-A-GU, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)