



SI9122ADQ-T1-E3 Information



For Reference Only

Part Number SI9122ADQ-T1-E3
Manufacturer Vishay Siliconix

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Controllers

Description IC REG CTRLR HALF-BRIDGE 20TSSOP

Package 20-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









SI9122ADQ-T1-E3 Specifications

Manufacturer Part Number	SI9122ADQ-T1-E3
Manufacturer	Vishay Siliconix
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	20-TSSOP (0.173", 4.40mm Width)
Series	-
Output Type	Transistor Driver
Function	Step-Up/Step-Down
Output Configuration	Positive, Isolation Capable
Topology	Half-Bridge
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	10.5 V ~ 13.2 V
Frequency - Switching	500kHz
Duty Cycle (Max)	92%
Synchronous Rectifier	Yes
Clock Sync	No
Serial Interfaces	-
Control Features	Current Limit, Sequencing, Soft Start
Operating Temperature	-40°C ~ 85°C (TA)
Package / Case	20-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	20-TSSOP
	Report errors?

SI9122ADQ-T1-E3 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

SI9122ADQ-T1-E3 Payment Methods



















SI9122ADQ-T1-E3 Shipping Methods













If you have any question about SI9122ADQ-T1-E3, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com