



### SIM3L146-C-GM Information



For Reference Only

Part Number SIM3L146-C-GM Manufacturer Silicon Labs

Category Integrated Circuits (ICs)
Embedded - Microcontrollers

**Description** IC MCU 32BIT 64KB FLASH 64QFN

Package 64-VFQFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **SIM3L146-C-GM Specifications**

Manufacturer Part Number	SIM3L146-C-GM	
Manufacturer	Silicon Labs	
Category	Integrated Circuits (ICs)	
	Embedded - Microcontrollers	
Package	64-VFQFN Exposed Pad	
Series	SiM3L1xx	
Core Processor	ARM? Cortex?-M3	
Core Size	32-Bit	
Speed	50MHz	
Connectivity	I2C, IrDA, SmartCard, SPI, UART/USART	
Peripherals	Brown-out Detect/Reset, DMA, LCD, POR, PWM, WDT	
Number of I/O	51	
Program Memory Size	64KB (64K x 8)	
Program Memory Type	FLASH	
EEPROM Size	-	
RAM Size	16K x 8	
Voltage - Supply (Vcc/Vdd)	1.8 V ~ 3.8 V	
Data Converters	A/D 23x10/12b, D/A 1x10b	
Oscillator Type	Internal	
Operating Temperature	-40°C ~ 85°C (TA)	
Mounting Type	-	
Package / Case	64-VFQFN Exposed Pad	
Supplier Device Package	64-QFN (9x9)	
		Report errors?

#### SIM3L146-C-GM Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SIM3L146-C-GM Payment Methods



















## SIM3L146-C-GM Shipping Methods













If you have any question about SIM3L146-C-GM, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com