

SN74HC7032D

SN74HC7032D Information

| west-indoener.com | SN74HC7032D Texas Instruments Integrated Circuits (ICs) Logic - Gates and Inverters IC GATE OR 4CH 2-INP 14-SOIC 14-SOIC (0.154", 3.90mm Width) For the pricing/inventory/lead time, please contact | |
|--------------------|---|-----------------|
| For Reference Only | us Website: https://www.heisener.com E-mail: salesdept@heisener.com | Request a Quote |



Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



SN74HC7032D Specifications

| Manufacturer Part Number | SN74HC7032D |
|-----------------------------------|---|
| Manufacturer | Texas Instruments |
| Category | Integrated Circuits (ICs) |
| | Logic - Gates and Inverters |
| Package | 14-SOIC (0.154", 3.90mm Width) |
| Series | 74HC |
| Logic Type | OR Gate |
| Number of Circuits | 4 |
| Number of Inputs | 2 |
| Features | Schmitt Trigger |
| Voltage - Supply | 2 V ~ 6 V |
| Current - Quiescent (Max) | 2μΑ |
| Current - Output High, Low | 5.2mA, 5.2mA |
| Logic Level - Low | 0.3 V ~ 1.2 V |
| Logic Level - High | 1.5 V ~ 4.2 V |
| Max Propagation Delay @ V, Max CL | 22ns @ 6V, 50pF |
| Operating Temperature | $-40^{\circ}\mathrm{C} \sim 85^{\circ}\mathrm{C}$ |
| Mounting Type | Surface Mount |
| Supplier Device Package | 14-SOIC |
| Package / Case | 14-SOIC (0.154", 3.90mm Width) |
| | Report errors? |

SN74HC7032D Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

SN74HC7032D Payment Methods





If you have any question about SN74HC7032D, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com