

**SN74LS132D Information**


For Reference Only

**Part Number** [SN74LS132D](#)  
**Manufacturer** Texas Instruments  
**Category** Integrated Circuits (ICs)  
[Logic - Gates and Inverters](#)  
**Description** IC GATE NAND 4CH 2-INP 14-SOIC  
**Package** 14-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**SN74LS132D Specifications**

Manufacturer Part Number	<a href="#">SN74LS132D</a>
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) <a href="#">Logic - Gates and Inverters</a>
Package	14-SOIC (0.154", 3.90mm Width)
Series	74LS
Logic Type	NAND Gate
Number of Circuits	4
Number of Inputs	2
Features	Schmitt Trigger
Voltage - Supply	4.75 V ~ 5.25 V
Current - Quiescent (Max)	11mA
Current - Output High, Low	400µA, 8mA
Logic Level - Low	0.5V
Logic Level - High	1.9V
Max Propagation Delay @ V, Max CL	22ns @ 5V, 15pF
Operating Temperature	0°C ~ 70°C
Mounting Type	Surface Mount
Supplier Device Package	14-SOIC
Package / Case	14-SOIC (0.154", 3.90mm Width)
	<a href="#">Report errors?</a>

## SN74LS132D Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SN74LS132D Payment Methods



## SN74LS132D Shipping Methods



If you have any question about SN74LS132D, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)