

**SN74LV20ADGVR Information**


For Reference Only

**Part Number** [SN74LV20ADGVR](#)  
**Manufacturer** Texas Instruments  
**Category** Integrated Circuits (ICs)  
[Logic - Gates and Inverters](#)  
**Description** IC GATE NAND 2CH 4-INP 14-TVSOP  
**Package** 14-TFSOP (0.173", 4.40mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**SN74LV20ADGVR Specifications**

Manufacturer Part Number	<a href="#">SN74LV20ADGVR</a>
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) <a href="#">Logic - Gates and Inverters</a>
Package	14-TFSOP (0.173", 4.40mm Width)
Series	74LV
Logic Type	NAND Gate
Number of Circuits	2
Number of Inputs	4
Features	-
Voltage - Supply	2 V ~ 5.5 V
Current - Quiescent (Max)	20µA
Current - Output High, Low	12mA, 12mA
Logic Level - Low	0.5V
Logic Level - High	1.5V
Max Propagation Delay @ V, Max CL	7ns @ 5V, 50pF
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Supplier Device Package	14-TVSOP
Package / Case	14-TFSOP (0.173", 4.40mm Width)

[Report errors?](#)

## SN74LV20ADGVR Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SN74LV20ADGVR Payment Methods



## SN74LV20ADGVR Shipping Methods



If you have any question about SN74LV20ADGVR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)