

**SPC5605BF1MLQ6 Information**


For Reference Only

**Part Number** [SPC5605BF1MLQ6](#)  
**Manufacturer** NXP  
**Category** Integrated Circuits (ICs)  
[Embedded - Microcontrollers](#)  
**Description** NXP 32-BIT MCU POWER ARCH CORE  
**Package** 144-LQFP  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**SPC5605BF1MLQ6 Specifications**

Manufacturer Part Number	<a href="#">SPC5605BF1MLQ6</a>
Manufacturer	NXP
Category	Integrated Circuits (ICs) <a href="#">Embedded - Microcontrollers</a>
Package	144-LQFP
Series	MPC56xx Qorivva
Core Processor	e200z0h
Core Size	32-Bit
Speed	64MHz
Connectivity	CANbus, I <sup>2</sup> C, LINbus, SCI, SPI
Peripherals	DMA, POR, PWM, WDT
Number of I/O	121
Program Memory Size	768KB (768K x 8)
Program Memory Type	FLASH
EEPROM Size	64K x 8
RAM Size	64K x 8
Voltage - Supply (Vcc/Vdd)	3 V ~ 5.5 V
Data Converters	A/D 15x10b, 5x12b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 125°C (TA)
Mounting Type	-
Package / Case	144-LQFP
Supplier Device Package	144-LQFP (20x20)

[Report errors?](#)

## SPC5605BF1MLQ6 Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SPC5605BF1MLQ6 Payment Methods



## SPC5605BF1MLQ6 Shipping Methods



If you have any question about SPC5605BF1MLQ6, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)