

# STM32F103R8H6TR

#### STM32F103R8H6TR Information

	Part Number	STM32F103R8H6TR	
	Manufacturer	STMicroelectronics	En la 1999
	Category	Integrated Circuits (ICs) Embedded - Microcontrollers	
	Description	IC MCU 32BIT 64KB FLASH 64TFBGA	- 3521
	Package	64-TFBGA	
		For the pricing/inventory/lead time, please contact	
For Reference Only		us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a

### **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



Quote

## STM32F103R8H6TR Specifications

Manufacturer Part Number	STM32F103R8H6TR		
Manufacturer	STMicroelectronics		
Category	Integrated Circuits (ICs)		
	Embedded - Microcontrollers		
Package	64-TFBGA		
Series	STM32 F1		
Core Processor	ARM? Cortex?-M3		
Core Size	32-Bit		
Speed	72MHz		
Connectivity	CAN, I2C, IrDA, LIN, SPI, UART/USART, USB		
Peripherals	DMA, Motor Control PWM, PDR, POR, PVD, PWM, Temp Sensor, WDT		
Number of I/O	51		
Program Memory Size	64KB (64K x 8)		
Program Memory Type	FLASH		
EEPROM Size	-		
RAM Size	20K x 8		
Voltage - Supply (Vcc/Vdd)	2 V ~ 3.6 V		
Data Converters	A/D 16x12b		
Oscillator Type	Internal		
Operating Temperature	-40°C ~ 85°C (TA)		
Mounting Type	-		
Package / Case	64-TFBGA		
Supplier Device Package	64-TFBGA (5x5)		
		Report errors?	

#### STM32F103R8H6TR Guarantees



**Quality Guarantees** 

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE BUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

#### STM32F103R8H6TR Payment Methods



If you have any question about STM32F103R8H6TR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com