

SVF332R3K1CKU2R

SVF332R3K1CKU2R Information

Heisener.com

Part Number SVF332R3K1CKU2R

Manufacturer NXP

Category Integrated Circuits (ICs)

Embedded - Microprocessors

Description VYBRID R 32-BIT MPU ARM CORTEX-

Package 176-LQFP Exposed Pad

For the pricing/inventory/lead time, please contact

us

For Reference Only

Website: https://www.heisener.com

E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









SVF332R3K1CKU2R Specifications

Manufacturer Part Number	SVF332R3K1CKU2R
Manufacturer	NXP
Category	Integrated Circuits (ICs)
	Embedded - Microprocessors
Package	176-LQFP Exposed Pad
Series	Vybrid, VF3xxR
Core Processor	ARM® Cortex®-A5 + Cortex®-M4
Number of Cores/Bus Width	2 Core, 32-Bit
Speed	266MHz, 133MHz
Co-Processors/DSP	Multimedia; NEON™ MPE
RAM Controllers	LPDDR2, DDR3, DRAM
Graphics Acceleration	Yes
Display & Interface Controllers	DCU, GPU, LCD, VideoADC, VIU
Ethernet	10/100 Mbps (2)
SATA	-
USB	USB 2.0 OTG + PHY (1)
Voltage - I/O	3.3V
Operating Temperature	-40°C ~ 85°C (TA)
Security Features	ARM TZ, Hashing, RNG, RTC, RTIC, Secure JTAG, SNVS, TZ ASC, TZ WDOG
Package / Case	176-LQFP Exposed Pad
Supplier Device Package	176-LQFP (24x24)
	Report errors?

SVF332R3K1CKU2R Guarantees



Ouality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

SVF332R3K1CKU2R Payment Methods





















SVF332R3K1CKU2R Shipping Methods













If you have any question about SVF332R3K1CKU2R, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com