

SY100EL51ZG-TR Information


For Reference Only

Part Number [SY100EL51ZG-TR](#)
Manufacturer Microchip Technology
Category Integrated Circuits (ICs)
[Logic - Flip Flops](#)
Description IC D-TYPE POS/NEG 8SOIC
Package 8-SOIC (0.154", 3.90mm Width)
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


SY100EL51ZG-TR Specifications

| | |
|-----------------------------------|---|
| Manufacturer Part Number | SY100EL51ZG-TR |
| Manufacturer | Microchip Technology |
| Category | Integrated Circuits (ICs) Logic - Flip Flops |
| Package | 8-SOIC (0.154", 3.90mm Width) |
| Series | 100EL |
| Function | Reset |
| Type | D-Type |
| Output Type | Differential |
| Number of Elements | 1 |
| Number of Bits per Element | 1 |
| Clock Frequency | 2.8GHz |
| Max Propagation Delay @ V, Max CL | - |
| Trigger Type | Positive, Negative |
| Current - Output High, Low | - |
| Voltage - Supply | -4.2 V ~ -5.5 V |
| Current - Quiescent (Iq) | 29mA |
| Input Capacitance | - |
| Operating Temperature | -40°C ~ 85°C (TA) |
| Mounting Type | Surface Mount |
| Package / Case | 8-SOIC (0.154", 3.90mm Width) |

[Report errors?](#)

SY100EL51ZG-TR Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

SY100EL51ZG-TR Payment Methods



SY100EL51ZG-TR Shipping Methods



If you have any question about SY100EL51ZG-TR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com