

# SY10EP05VKG-TR

#### SY10EP05VKG-TR Information

. Re	Part Number	SY10EP05VKG-TR	
-	Manufacturer	Microchip Technology	
www.helsener.com	Category	Integrated Circuits (ICs) Logic - Gates and Inverters - Multi-Function, Configurable	
15	Description	IC GATE AND/NAND 2INP DIFF 8MSOP	10.5x2
	Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	回該設施
		For the pricing/inventory/lead time, please contact	
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

#### **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



### SY10EP05VKG-TR Specifications

Manufacturer Part Number	SY10EP05VKG-TR	
Manufacturer	Microchip Technology	
Category	Integrated Circuits (ICs)	
	Logic - Gates and Inverters - Multi-Function, Configurable	
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Series	10EP	
Logic Type	AND/NAND Gate	
Number of Circuits	1	
Number of Inputs	2 Input (1, 1)	
Schmitt Trigger Input	No	
Output Type	Differential	
Current - Output High, Low	-	
Voltage - Supply	3 V ~ 5.5 V	
Operating Temperature	$-40^{\circ}\mathrm{C} \sim 85^{\circ}\mathrm{C}$	
Mounting Type	Surface Mount	
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)	
Supplier Device Package	8-MSOP	
	Report error	s?

#### SY10EP05VKG-TR Guarantees



**Quality Guarantees** 

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE BUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

#### SY10EP05VKG-TR Payment Methods



## SY10EP05VKG-TR Shipping Methods



If you have any question about SY10EP05VKG-TR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com