

**SY58052AUMG-TR Information**


For Reference Only

**Part Number** [SY58052AUMG-TR](#)  
**Manufacturer** Microchip Technology  
**Category** Integrated Circuits (ICs)  
 Clock/Timing - Clock Generators, PLLs,  
 Frequency Synthesizers  
**Description** IC SYNTHESIZER DATA-CLOCK 16QFN  
**Package** 16-VFQFN Exposed Pad  
 For the pricing/inventory/lead time, please contact  
 us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**SY58052AUMG-TR Specifications**

Manufacturer Part Number	<a href="#">SY58052AUMG-TR</a>
Manufacturer	Microchip Technology
Category	Integrated Circuits (ICs) <a href="#">Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers</a>
Package	16-VFQFN Exposed Pad
Series	Precision Edge?
Type	-
PLL	No
Input	CML, PECL, Data
Output	CML
Number of Circuits	1
Ratio - Input:Output	2:1
Differential - Input:Output	Yes/Yes
Frequency - Max	10.7GHz
Divider/Multiplier	No/No
Voltage - Supply	2.375 V ~ 3.63 V
Operating Temperature	-40°C ~ 85°C
Mounting Type	Surface Mount
Package / Case	16-VFQFN Exposed Pad
Supplier Device Package	16-QFN (3x3)
<a href="#">Report errors?</a>	

## SY58052AUMG-TR Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## SY58052AUMG-TR Payment Methods



## SY58052AUMG-TR Shipping Methods



If you have any question about SY58052AUMG-TR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)