

# TCR4DG18,LF

### **TCR4DG18,LF Information**

www.heisener.com	TCR4DG18,LF Toshiba Semiconductor and Storage Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear X34 LDO REGULATORS VOUT: 1.8V IO 4-XFBGA, CSPBGA For the pricing/inventory/lead time_please contact	
For Reference Only	For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



### **TCR4DG18,LF Specifications**

Manufacturer Part Number	TCR4DG18,LF
Manufacturer	Toshiba Semiconductor and Storage
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - Linear
Package	4-XFBGA, CSPBGA
Series	TCR4DG
Output Configuration	Positive
Output Type	Fixed
Number of Regulators	1
Voltage - Input (Max)	5.5V
Voltage - Output (Min/Fixed)	1.8V
Voltage - Output (Max)	-
Voltage Dropout (Max)	0.473V @ 420mA
Current - Output	420mA
Current - Quiescent (Iq)	-
Current - Supply (Max)	-
PSRR	70dB (1kHz)
Control Features	Enable
Protection Features	Over Current, Over Temperature, Short Circuit
Operating Temperature	$-40^{\circ}\mathrm{C} \sim 85^{\circ}\mathrm{C}$
Mounting Type	Surface Mount
Package / Case	4-XFBGA, CSPBGA
Supplier Device Package	4-WCSPE (0.65x0.65)
	Report error

#### **TCR4DG18,LF** Guarantees



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

#### **TCR4DG18,LF** Payment Methods





If you have any question about TCR4DG18,LF, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com