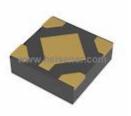




TCR5BM12A,L3F Information



For Reference Only

Part Number TCR5BM12A,L3F

Manufacturer Toshiba Semiconductor and Storage

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - Linear

Description IC REG LIN 1.2V 500MA 5DFNB

Package 4-XDFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









TCR5BM12A,L3F Specifications

	Report errors?
Supplier Device Package	5-DFNB (1.2x1.2)
Package / Case	4-XDFN Exposed Pad
Mounting Type	Surface Mount
Operating Temperature	-40°C ~ 85°C
Protection Features	Over Current, Over Temperature
Control Features	Current Limit, Enable
PSRR	98dB (1kHz)
Current - Supply (Max)	-
Current - Quiescent (Iq)	36μΑ
Current - Output	500mA
Voltage Dropout (Max)	0.15V @ 500mA
Voltage - Output (Max)	-
Voltage - Output (Min/Fixed)	1.2V
Voltage - Input (Max)	5.5V
Number of Regulators	1
Output Type	Fixed
Output Configuration	Positive
Series	TCR5BM
Package	4-XDFN Exposed Pad
	PMIC - Voltage Regulators - Linear
Category	Integrated Circuits (ICs)
Manufacturer	Toshiba Semiconductor and Storage
Manufacturer Part Number	TCR5BM12A,L3F

TCR5BM12A,L3F Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

TCR5BM12A,L3F Payment Methods





















TCR5BM12A,L3F Shipping Methods













If you have any question about TCR5BM12A,L3F, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com