



TL5001CDG4 Information



For Reference Only

Part Number TL5001CDG4

Manufacturer Texas Instruments

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Controllers

Description IC REG CTRLR BCK/BST/FLYBK 8SOIC

Package 8-SOIC (0.154", 3.90mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









TL5001CDG4 Specifications

Manufacturer Part Number	TL5001CDG4	
Manufacturer	Texas Instruments	
Category	Integrated Circuits (ICs)	
	PMIC - Voltage Regulators - DC DC Switching Controllers	
Package	8-SOIC (0.154", 3.90mm Width)	
Series	-	
Output Type	Transistor Driver	
Function	Step-Up, Step-Down, Step-Up/Step-Down	
Output Configuration	Positive	
Topology	Buck, Boost, Flyback	
Number of Outputs	1	
Output Phases	1	
Voltage - Supply (Vcc/Vdd)	3.6 V ~ 40 V	
Frequency - Switching	20kHz ~ 500kHz	
Duty Cycle (Max)	100%	
Synchronous Rectifier	No	
Clock Sync	No	
Serial Interfaces	-	
Control Features	Dead Time Control, Frequency Control	
Operating Temperature	-20°C ~ 85 °C (TA)	
Package / Case	8-SOIC (0.154", 3.90mm Width)	
Supplier Device Package	8-SOIC	
	Report error	s?

TL5001CDG4 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

TL5001CDG4 Payment Methods



















TL5001CDG4 Shipping Methods













If you have any question about TL5001CDG4, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com