

**TLV0834CDR Information**


For Reference Only

**Part Number** [TLV0834CDR](#)  
**Manufacturer** Texas Instruments  
**Category** Integrated Circuits (ICs)  
[Data Acquisition - Analog to Digital Converters \(ADC\)](#)  
**Description** IC ADC 8-BIT 4CH SER 3V 14-SOIC  
**Package** 14-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**TLV0834CDR Specifications**

Manufacturer Part Number	<a href="#">TLV0834CDR</a>
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) <a href="#">Data Acquisition - Analog to Digital Converters (ADC)</a>
Package	14-SOIC (0.154", 3.90mm Width)
Series	-
Number of Bits	8
Sampling Rate (Per Second)	41k
Number of Inputs	2, 3, 4
Input Type	Differential, Pseudo-Differential, Single Ended
Data Interface	SPI
Configuration	MUX-ADC
Ratio - S/H:ADC	-
Number of A/D Converters	1
Architecture	SAR
Reference Type	External
Voltage - Supply, Analog	2.7 V ~ 3.6 V
Voltage - Supply, Digital	2.7 V ~ 3.6 V
Features	-
Operating Temperature	0°C ~ 70°C
Package / Case	14-SOIC (0.154", 3.90mm Width)
Supplier Device Package	14-SOIC
Mounting Type	-

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## TLV0834CDR Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## TLV0834CDR Payment Methods



## TLV0834CDR Shipping Methods



If you have any question about TLV0834CDR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

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