



### **TLV5614IPWG4 Information**

Part Number TLV5614IPWG4 Manufacturer Texas Instruments Category Integrated Circuits (ICs)

Data Acquisition - Digital to Analog Converters

(DAC)

Description IC 12-BIT QUAD SER DAC 16-TSSOP **Package** 16-TSSOP (0.173", 4.40mm Width)

For the pricing/inventory/lead time, please contact

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **TLV5614IPWG4 Specifications**

Manufacturer Part Number	TLV5614IPWG4	
Manufacturer	Texas Instruments	
Category	Integrated Circuits (ICs)	
	Data Acquisition - Digital to Analog Converters (DAC)	
Package	16-TSSOP (0.173", 4.40mm Width)	
Series	-	
Number of Bits	12	
Number of D/A Converters	4	
Settling Time	20μs	
Output Type	Voltage - Buffered	
Differential Output	No	
Data Interface	SPI	
Reference Type	External	
Voltage - Supply, Analog	2.7 V ~ 3.3 V, 5V	
Voltage - Supply, Digital	2.7 V ~ 3.3 V, 5V	
INL/DNL (LSB)	$\pm 1.5, \pm 0.5$	
Architecture	String DAC	
Operating Temperature	-40°C ~ 85°C	
Package / Case	16-TSSOP (0.173", 4.40mm Width)	
Supplier Device Package	16-TSSOP	
Mounting Type	-	
	Report error	rs?

#### **TLV5614IPWG4 Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# **TLV5614IPWG4 Payment Methods**



















## **TLV5614IPWG4 Shipping Methods**













If you have any question about TLV5614IPWG4, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com