

TLV70518YFPR

Request a Quote

TLV70518YFPR Information

www.bersener.com		TLV70518YFPR Texas Instruments
	Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - Linear
	Description	IC REG LINEAR 1.8V 200MA 4DSBGA
	Package	4-XFBGA, DSBGA
		For the pricing/inventory/lead time, please contact
For Reference Only		us Website: https://www.heisener.com E-mail: salesdept@heisener.com

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



TLV70518YFPR Specifications

Manufacturer Part Number	TLV70518YFPR		
Manufacturer	Texas Instruments		
Category	Integrated Circuits (ICs)		
	PMIC - Voltage Regulators - Linear		
Package	4-XFBGA, DSBGA		
Series	-		
Output Configuration	Positive		
Output Type	Fixed		
Number of Regulators	1		
Voltage - Input (Max)	5.5V		
Voltage - Output (Min/Fixed)	1.8V		
Voltage - Output (Max)	-		
Voltage Dropout (Max)	0.25V @ 200mA		
Current - Output	200mA		
Current - Quiescent (Iq)	-		
Current - Supply (Max)	55μΑ ~ 315μΑ		
PSRR	80dB ~ 55dB (10kHz ~ 1MHz)		
Control Features	Enable		
Protection Features	Over Current, Over Temperature, Reverse Polarity, Under Voltage Lockout (UVLO)		
Operating Temperature	-40°C ~ 125°C		
Mounting Type	Surface Mount		
Package / Case	4-XFBGA, DSBGA		
Supplier Device Package	4-DSBGA (0.8x0.8)		
	Report errors?		

TLV70518YFPR Guarantees



Quality Guarantees

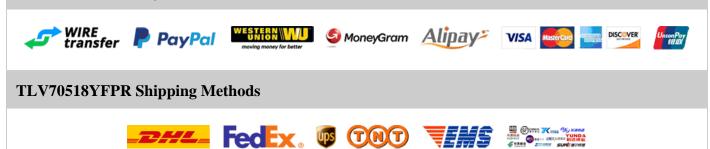
We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE EUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

TLV70518YFPR Payment Methods



If you have any question about TLV70518YFPR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com