

TPS40000DGQG4 Information


For Reference Only

Part Number [TPS40000DGQG4](#)
Manufacturer Texas Instruments
Category Integrated Circuits (ICs)
[PMIC - Voltage Regulators - DC DC Switching Controllers](#)
Description IC REG CTRLR BUCK 10MSOP
Package 10-TFSOP, 10-MSOP (0.118", 3.00mm Width)
 Exposed Pad
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


TPS40000DGQG4 Specifications

Manufacturer Part Number	TPS40000DGQG4
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers
Package	10-TFSOP, 10-MSOP (0.118", 3.00mm Width) Exposed Pad
Series	-
Output Type	Transistor Driver
Function	Step-Down
Output Configuration	Positive
Topology	Buck
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	2.25 V ~ 5.5 V
Frequency - Switching	300kHz
Duty Cycle (Max)	94%
Synchronous Rectifier	Yes
Clock Sync	No
Serial Interfaces	-
Control Features	Current Limit, Enable, Soft Start
Operating Temperature	-40°C ~ 85°C (TA)
Package / Case	10-TFSOP, 10-MSOP (0.118", 3.00mm Width) Exposed Pad
Supplier Device Package	10-MSOP-PowerPad

[Report errors?](#)

TPS40000DGQG4 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

TPS40000DGQG4 Payment Methods



TPS40000DGQG4 Shipping Methods



If you have any question about TPS40000DGQG4, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com