



TPS40051PWPG4 Information



For Reference Only

Part Number TPS40051PWPG4

Manufacturer Texas Instruments

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Controllers

Description IC REG CTRLR BUCK 16HTSSOP

Package 16-TSSOP (0.173", 4.40mm Width) Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









TPS40051PWPG4 Specifications

Manufacturer Part Number	TPS40051PWPG4
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	16-TSSOP (0.173", 4.40mm Width) Exposed Pad
Series	-
Output Type	Transistor Driver
Function	Step-Down
Output Configuration	Positive
Topology	Buck
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	8 V ~ 40 V
Frequency - Switching	500kHz
Duty Cycle (Max)	94%
Synchronous Rectifier	Yes
Clock Sync	Yes
Serial Interfaces	-
Control Features	Current Limit, Enable, Frequency Control, Soft Start
Operating Temperature	-40°C ~ 85°C (TA)
Package / Case	16-TSSOP (0.173", 4.40mm Width) Exposed Pad
Supplier Device Package	16-HTSSOP
	Report errors?

TPS40051PWPG4 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

TPS40051PWPG4 Payment Methods



















TPS40051PWPG4 Shipping Methods













If you have any question about TPS40051PWPG4, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com