

TPS40195PWRG4

TPS40195PWRG4 Information

Mar		TPS40195PWRG4 Texas Instruments Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers	
pretent Des	cription	IC REG CTRLR BUCK 16TSSOP	1 N N N N N
Pac	kage	16-TSSOP (0.173", 4.40mm Width)	
		For the pricing/inventory/lead time, please contact	1218-0628
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



TPS40195PWRG4 Specifications

Manufacturer Part Number	TPS40195PWRG4			
Manufacturer	Texas Instruments			
Category	Integrated Circuits (ICs)			
	PMIC - Voltage Regulators - DC DC Switching Controllers			
Package	16-TSSOP (0.173", 4.40mm Width)			
Series	-			
Output Type	Transistor Driver			
Function	Step-Down			
Output Configuration	Positive			
Topology	Buck			
Number of Outputs	1			
Output Phases	1			
Voltage - Supply (Vcc/Vdd)	4.5 V ~ 20 V			
Frequency - Switching	100kHz ~ 600kHz			
Duty Cycle (Max)	85%			
Synchronous Rectifier	Yes			
Clock Sync	Yes			
Serial Interfaces	-			
Control Features	Current Limit, Enable, Frequency Control, Power Good, Soft Start			
Operating Temperature	-40°C ~ 125°C (TJ)			
Package / Case	16-TSSOP (0.173", 4.40mm Width)			
Supplier Device Package	16-TSSOP			
		Report errors?		

Report errors?

TPS40195PWRG4 Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

TPS40195PWRG4 Payment Methods



If you have any question about TPS40195PWRG4, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com