

TS5A2066DCURG4

TS5A2066DCURG4 Information

	TS5A2066DCURG4 Texas Instruments Integrated Circuits (ICs) Interface - Analog Switches, Multiplexers, Demultiplexers	
Description	IC SWITCH DUAL SPST US8	1.1962,556 (
Package	8-VFSOP (0.091", 2.30mm Width)	
For Reference Only	For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



TS5A2066DCURG4 Specifications

Manufacturer Part Number	TS5A2066DCURG4
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs)
	Interface - Analog Switches, Multiplexers, Demultiplexers
Package	8-VFSOP (0.091", 2.30mm Width)
Series	-
Switch Circuit	SPST - NO
Multiplexer/Demultiplexer Circuit	1:1
Number of Circuits	2
On-State Resistance (Max)	10 Ohm
Channel-to-Channel Matching (Ron)	1 Ohm
Voltage - Supply, Single (V+)	1.65 V ~ 5.5 V
Voltage - Supply, Dual (V±)	-
Switch Time (Ton, Toff) (Max)	5.8ns, 3.6ns
-3db Bandwidth	300MHz
Charge Injection	1pC
Channel Capacitance (CS(off), CD(off))	5.5pF, 5.5pF
Current - Leakage (IS(off)) (Max)	30nA
Crosstalk	-68dB @ 10MHz
Operating Temperature	-40°C ~ 85°C (TA)
Package / Case	8-VFSOP (0.091", 2.30mm Width)
Supplier Device Package	8-VSSOP
	Report errors?

TS5A2066DCURG4 Guarantees



Quality Guarantees

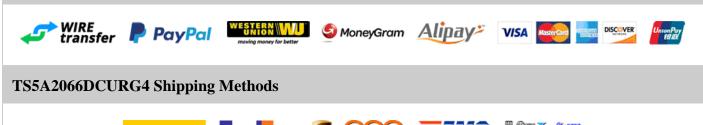
We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE UARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

TS5A2066DCURG4 Payment Methods





If you have any question about TS5A2066DCURG4, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com