

UC2843AN

UC2843AN Information



| rt Number | UC2843AN |
|-------------|--|
| anufacturer | ON Semiconductor |
| tegory | Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers |
| scription | IC REG CTRLR BUCK 8DIP |
| ckage | 8-DIP (0.300", 7.62mm) |
| | For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com E-mail: salesdept@heisener.com |



Request a Quote

Certified Quality

For Reference Only

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



UC2843AN Specifications

| Manufacturer Part Number | UC2843AN |
|----------------------------|---|
| Manufacturer | ON Semiconductor |
| Category | Integrated Circuits (ICs) |
| | PMIC - Voltage Regulators - DC DC Switching Controllers |
| Package | 8-DIP (0.300", 7.62mm) |
| Series | - |
| Output Type | Transistor Driver |
| Function | Step-Down |
| Output Configuration | Positive, Isolation Capable |
| Topology | Buck |
| Number of Outputs | 1 |
| Output Phases | 1 |
| Voltage - Supply (Vcc/Vdd) | 8.4 V ~ 30 V |
| Frequency - Switching | 500kHz |
| Duty Cycle (Max) | 96% |
| Synchronous Rectifier | Yes |
| Clock Sync | No |
| Serial Interfaces | - |
| Control Features | Frequency Control |
| Operating Temperature | -40°C ~ 85°C (TA) |
| Package / Case | 8-DIP (0.300", 7.62mm) |
| Supplier Device Package | 8-PDIP |
| | Report errors? |

UC2843AN Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.



If you have any question about UC2843AN, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com