

UC3843BD1R2

UC3843BD1R2 Information

	WWW.Innsener.com	Part Number Manufacturer Category	UC3843BD1R2 ON Semiconductor Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers	
		Description	IC REG CTRLR FLYBACK 8SOIC	- 2001.642 I
		Package	8-SOIC (0.154", 3.90mm Width)	- 国际通知法
	For Reference Only		For the pricing/inventory/lead time, please contact us	
			Website: https://www.heisener.com	Request a Quote
			E-mail: salesdept@heisener.com	

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



UC3843BD1R2 Specifications

Manufacturer Part Number	UC3843BD1R2
Manufacturer	ON Semiconductor
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	8-SOIC (0.154", 3.90mm Width)
Series	- · · · · · · · · · · · · · · · · · · ·
Output Type	Transistor Driver
Function	Step-Up/Step-Down
Output Configuration	Positive, Isolation Capable
Topology	Flyback
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	7.6 V ~ 30 V
Frequency - Switching	500kHz
Duty Cycle (Max)	96%
Synchronous Rectifier	No
Clock Sync	No
Serial Interfaces	-
Control Features	Frequency Control
Operating Temperature	0°C ~ 70°C (TA)
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOIC
	Report errors?

UC3843BD1R2 Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE BUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

UC3843BD1R2 Payment Methods



UC3843BD1R2 Shipping Methods



If you have any question about UC3843BD1R2, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com