

UC3846DW Information


For Reference Only

Part Number [UC3846DW](#)
Manufacturer Texas Instruments
Category Integrated Circuits (ICs)
[PMIC - Voltage Regulators - DC DC Switching Controllers](#)
Description IC REG CTRLR MULT TOP 16SOIC
Package 16-SOIC (0.295", 7.50mm Width)
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


UC3846DW Specifications

Manufacturer Part Number	UC3846DW
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers
Package	16-SOIC (0.295", 7.50mm Width)
Series	-
Output Type	Transistor Driver
Function	Step-Up, Step-Down, Step-Up/Step-Down
Output Configuration	Positive
Topology	Buck, Boost, Flyback, Forward Converter, Full-Bridge, Half-Bridge, Push-Pull
Number of Outputs	2
Output Phases	1
Voltage - Supply (Vcc/Vdd)	6.95 V ~ 40 V
Frequency - Switching	500kHz
Duty Cycle (Max)	-
Synchronous Rectifier	Yes
Clock Sync	Yes
Serial Interfaces	-
Control Features	Current Limit, Enable, Frequency Control, Soft Start
Operating Temperature	0°C ~ 70°C (TA)
Package / Case	16-SOIC (0.295", 7.50mm Width)
Supplier Device Package	16-SOIC

[Report errors?](#)

UC3846DW Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

UC3846DW Payment Methods



UC3846DW Shipping Methods



If you have any question about UC3846DW, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: salesdept@heisener.com