

**UCC27200DR Information**


For Reference Only

**Part Number** [UCC27200DR](#)  
**Manufacturer** Texas Instruments  
**Category** Integrated Circuits (ICs)  
     [PMIC - Gate Drivers](#)  
**Description** IC DVR HIGH/LOW SIDE 3A 8SOIC  
**Package** 8-SOIC (0.154", 3.90mm Width)  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**UCC27200DR Specifications**

Manufacturer Part Number	<a href="#">UCC27200DR</a>
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs) <a href="#">PMIC - Gate Drivers</a>
Package	8-SOIC (0.154", 3.90mm Width)
Series	-
Driven Configuration	Half-Bridge
Channel Type	Independent
Number of Drivers	2
Gate Type	N-Channel MOSFET
Voltage - Supply	8 V ~ 17 V
Logic Voltage - VIL, VIH	3V, 8V
Current - Peak Output (Source, Sink)	3A, 3A
Input Type	Non-Inverting
High Side Voltage - Max (Bootstrap)	120V
Rise / Fall Time (Typ)	8ns, 7ns
Operating Temperature	-40°C ~ 140°C (TJ)
Mounting Type	Surface Mount
Package / Case	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package	8-SOIC

[Report errors?](#)

## UCC27200DR Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## UCC27200DR Payment Methods



## UCC27200DR Shipping Methods



If you have any question about UCC27200DR, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)