

# UCC28231DRNR

### **UCC28231DRNR Information**

110 110 110 100 100 100 100 100 100 100		UCC28231DRNR Texas Instruments Integrated Circuits (ICs) PMIC - Voltage Regulators - DC DC Switching Controllers IC REG CTRLR MULT TOP 12-SON	
For Reference Only	Package	12-UFDFN Exposed Pad For the pricing/inventory/lead time, please contact us Website: https://www.heisener.com	Request a Quote
		E-mail: salesdept@heisener.com	Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



# **UCC28231DRNR Specifications**

Manufacturer Part Number	UCC28231DRNR
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	12-UFDFN Exposed Pad
Series	-
Output Type	Transistor Driver
Function	Step-Up/Step-Down
Output Configuration	Positive
Topology	Full-Bridge, Half-Bridge, Push-Pull
Number of Outputs	4
Output Phases	1
Voltage - Supply (Vcc/Vdd)	7 V ~ 17 V
Frequency - Switching	100kHz ~ 550kHz
Duty Cycle (Max)	-
Synchronous Rectifier	No
Clock Sync	No
Serial Interfaces	-
Control Features	Frequency Control, Soft Start
Operating Temperature	-55°C ~ 125°C (TJ)
Package / Case	12-UFDFN Exposed Pad
Supplier Device Package	12-USON (3x2)
	Report errors?

#### **UCC28231DRNR Guarantees**



Quality Guarantees

We provide 90 days warranty. \* If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

# SERVICE BUARANTEE

#### **Service Guarantees**

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

# **UCC28231DRNR Payment Methods**



## **UCC28231DRNR** Shipping Methods



If you have any question about UCC28231DRNR, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com