



UCC28C40DGKRG4 Information



For Reference Only

Part Number UCC28C40DGKRG4
Manufacturer Texas Instruments
Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Controllers

Description IC REG CTRLR MULT TOP 8VSSOP **Package** 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









UCC28C40DGKRG4 Specifications

Manufacturer Part Number	UCC28C40DGKRG4
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Series	-
Output Type	Transistor Driver
Function	Step-Up, Step-Up/Step-Down
Output Configuration	Positive
Topology	Boost, Flyback, Forward Converter
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	6.6 V ~ 18 V
Frequency - Switching	-
Duty Cycle (Max)	96%
Synchronous Rectifier	No
Clock Sync	No
Serial Interfaces	-
Control Features	Frequency Control
Operating Temperature	-40°C ~ 105°C (TJ)
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width)
Supplier Device Package	8-VSSOP
	Report errors

UCC28C40DGKRG4 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

UCC28C40DGKRG4 Payment Methods



















UCC28C40DGKRG4 Shipping Methods













If you have any question about UCC28C40DGKRG4, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com