



### **UCC37323DGNRG4** Information



For Reference Only

Part Number UCC37323DGNRG4

Manufacturer Texas Instruments

Category Integrated Circuits (ICs)
PMIC - Gate Drivers

**Description** IC DUAL HS PWR FET DRVR 8-MSOP 8-TSSOP, 8-MSOP (0.118", 3.00mm Width)

Package Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

# **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## **UCC37323DGNRG4** Specifications

Manufacturer Part Number	UCC37323DGNRG4
Manufacturer	Texas Instruments
Category	Integrated Circuits (ICs)
	PMIC - Gate Drivers
Package	8-TSSOP, 8-MSOP (0.118", 3.00mm Width) Exposed Pad
Series	-
Driven Configuration	Low-Side
Channel Type	Independent
Number of Drivers	2
Gate Type	N-Channel, P-Channel MOSFET
Voltage - Supply	4.5 V ~ 15 V
Logic Voltage - VIL, VIH	1V, 2V
Current - Peak Output (Source, Sink)	4A, 4A
Input Type	Inverting
High Side Voltage - Max (Bootstrap)	-
Rise / Fall Time (Typ)	20ns, 15ns
Operating Temperature	-55°C ~ 150°C (TJ)
Mounting Type	Surface Mount
Package / Case	8-TSSOP, 8-MSOP (0.118", 3.00mm Width) Exposed Pad
Supplier Device Package	8-MSOP-PowerPad
	Report errors?

#### **UCC37323DGNRG4** Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### **UCC37323DGNRG4** Payment Methods









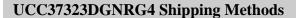
























If you have any question about UCC37323DGNRG4, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com