

X4645V8-2.7A

X4645V8-2.7A Information

www.bensener.com	Part Number Manufacturer	X4645V8-2.7A	
	Category	Intersti Integrated Circuits (ICs) PMIC - Supervisors	
	Description	IC SUPERVISOR CPU 64K EE 8-TSSOP	- 323 -325-3
	Package	8-TSSOP (0.173", 4.40mm Width)	
		For the pricing/inventory/lead time, please contact us	E1/24224
For Reference Only		Website: https://www.heisener.com E-mail: salesdept@heisener.com	Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.



X4645V8-2.7A Specifications

Manufacturer Part Number	X4645V8-2.7A
Manufacturer	Intersil
Category	Integrated Circuits (ICs)
	PMIC - Supervisors
Package	8-TSSOP (0.173", 4.40mm Width)
Series	-
Туре	Simple Reset/Power-On Reset
Number of Voltages Monitored	1
Output	-
Reset	Active High
Reset Timeout	100 ms Minimum
Voltage - Threshold	2.92V
Operating Temperature	$0^{\circ}C \sim 70^{\circ}C$ (TA)
Mounting Type	Surface Mount
Package / Case	8-TSSOP (0.173", 4.40mm Width)
Supplier Device Package	8-TSSOP
	Report errors?

X4645V8-2.7A Guarantees



Quality Guarantees

We provide 90 days warranty. * If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.

SERVICE GUARANTEE

Service Guarantees

We guarantee 100% customer satisfaction. Our experienced sales team and tech support team back our services to satisfy all our customers.

X4645V8-2.7A Payment Methods Image: WIRE transfer PayPal Image: Constraint of the tetter X4645V8-2.7A Shipping Methods Image: Constraint of the tetter X4645V8-2.7A Shipping Methods Image: Constraint of the tetter Image: Constraint of tetter X4645V8-2.7A Shipping Methods Image: Constraint of tetter Image: Constettet of tetter

If you have any question about X4645V8-2.7A, please do not hesitate to contact us! Website: https://www.heisener.com E-mail: salesdept@heisener.com