



#### XC9282B10E1R-G Information

Heisener.com

For Reference Only

Part Number XC9282B10E1R-G

Manufacturer Torex Semiconductor Ltd Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Regulators

**Description** HISAT-COT CONTROL EXTREMELY SMAL

Package 6-XFLGA

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

### **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# XC9282B10E1R-G Specifications

Manufacturer Part Number	XC9282B10E1R-G
Manufacturer	Torex Semiconductor Ltd
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Regulators
Package	6-XFLGA
Series	XC9282
Function	Step-Down
Output Configuration	Positive
Topology	Buck
Output Type	Fixed
Number of Outputs	1
Voltage - Input (Min)	2.5V
Voltage - Input (Max)	5.5V
Voltage - Output (Min/Fixed)	1V
Voltage - Output (Max)	-
Current - Output	600mA
Frequency - Switching	6MHz
Synchronous Rectifier	Yes
Operating Temperature	$-40^{\circ}\text{C} \sim 105^{\circ}\text{C} \text{ (TA)}$
Mounting Type	Surface Mount
Package / Case	6-XFLGA
Supplier Device Package	6-LGA-B01 (1.2x1.2)
	Report errors?

### XC9282B10E1R-G Guarantees



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

# XC9282B10E1R-G Payment Methods





















# XC9282B10E1R-G Shipping Methods













If you have any question about XC9282B10E1R-G, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com