

Heisener.com

# XCZU3EG-L1SFVA625I

#### XCZU3EG-L1SFVA625I Information

Part Number XCZU3EG-L1SFVA625I

Manufacturer Xilinx Inc.

Category Integrated Circuits (ICs)

Embedded - System On Chip (SoC)

**Description** IC FPGA 180 I/O 625FCBGA

625-BFBGA, FCBGA

**Package** For the pricing/inventory/lead time, please contact

Website: https://www.heisener.com For Reference Only

E-mail: salesdept@heisener.com



Request a Quote

### **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









## XCZU3EG-L1SFVA625I Specifications

Manufacturer Part Number	XCZU3EG-L1SFVA625I
Manufacturer	Xilinx Inc.
Category	Integrated Circuits (ICs)
	Embedded - System On Chip (SoC)
Package	625-BFBGA, FCBGA
Series	Zynq? UltraScale+? MPSoC EG
Architecture	MCU, FPGA
Core Processor	Quad ARM? Cortex?-A53 MPCore? with CoreSight?, Dual ARM?Cortex?-R5 with CoreSight?
Flash Size	-
RAM Size	256KB
Peripherals	DMA, WDT
Connectivity	CAN, EBI/EMI, Ethernet, I2C, MMC/SD/SDIO, SPI, UART/USART, USB OTG
Speed	600MHz, 1.5GHz
Primary Attributes	Zynq?UltraScale+? FPGA, 154K+ Logic Cells
Operating Temperature	-40°C ~ 100°C (TJ)
Package / Case	625-BFBGA, FCBGA
Supplier Device Package	625-FCBGA (21x21)
	Report errors?

### **XCZU3EG-L1SFVA625I Guarantees**



#### **Ouality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

### XCZU3EG-L1SFVA625I Payment Methods



















## **XCZU3EG-L1SFVA625I Shipping Methods**













If you have any question about XCZU3EG-L1SFVA625I, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com