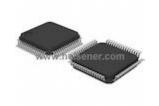


XMC1402F064X0200AAXUMA1

XMC1402F064X0200AAXUMA1 Information



For Reference Only

Part Number XMC1402F064X0200AAXUMA1

Manufacturer Infineon Technologies

Category Integrated Circuits (ICs)
Embedded - Microcontrollers

Description IC MCU 32BIT 200KB FLASH 64LQFP

Package 64-LQFP

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com Request a Quote



Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









XMC1402F064X0200AAXUMA1 Specifications

Manufacturer Part Number	XMC1402F064X0200AAXUMA1
Manufacturer	Infineon Technologies
Category	Integrated Circuits (ICs)
	Embedded - Microcontrollers
Package	64-LQFP
Series	XMC1000
Core Processor	ARM? Cortex?-M0
Core Size	32-Bit
Speed	48MHz
Connectivity	I2C, LIN, SPI, UART/USART
Peripherals	Brown-out Detect/Reset, I2S, POR, PWM, WDT
Number of I/O	48
Program Memory Size	200KB (200K x 8)
Program Memory Type	FLASH
EEPROM Size	-
RAM Size	16K x 8
Voltage - Supply (Vcc/Vdd)	1.8 V ~ 5.5 V
Data Converters	A/D 12x12b
Oscillator Type	Internal
Operating Temperature	-40°C ~ 105°C (TA)
Mounting Type	-
Package / Case	64-LQFP
Supplier Device Package	PG-LQFP-64
	Report errors?

XMC1402F064X0200AAXUMA1 Guarantees



Ouality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

XMC1402F064X0200AAXUMA1 Payment Methods



















XMC1402F064X0200AAXUMA1 Shipping Methods













If you have any question about XMC1402F064X0200AAXUMA1, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com