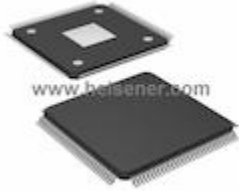


**XU212-256-TQ128-I20 Information**


For Reference Only

**Part Number** [XU212-256-TQ128-I20](#)  
**Manufacturer** XMOS  
**Category** Integrated Circuits (ICs)  
[Embedded - Microcontrollers](#)  
**Description** IC MCU 32BIT ROMLESS 128TQFP  
**Package** 128-TQFP Exposed Pad  
 For the pricing/inventory/lead time, please contact us  
 Website: <https://www.heisener.com>  
 E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)


[Request a Quote](#)
**Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


**XU212-256-TQ128-I20 Specifications**

Manufacturer Part Number	<a href="#">XU212-256-TQ128-I20</a>
Manufacturer	XMOS
Category	Integrated Circuits (ICs) <a href="#">Embedded - Microcontrollers</a>
Package	128-TQFP Exposed Pad
Series	XU
Core Processor	XCore
Core Size	32-Bit 12-Core
Speed	2000MIPS
Connectivity	USB
Peripherals	-
Number of I/O	81
Program Memory Size	-
Program Memory Type	ROMless
EEPROM Size	-
RAM Size	256K x 8
Voltage - Supply (Vcc/Vdd)	0.95 V ~ 3.6 V
Data Converters	-
Oscillator Type	External
Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	-
Package / Case	128-TQFP Exposed Pad
Supplier Device Package	128-TQFP (14x14)

[Report errors?](#)

## XU212-256-TQ128-I20 Guarantees



### Quality Guarantees

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



### Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## XU212-256-TQ128-I20 Payment Methods



## XU212-256-TQ128-I20 Shipping Methods



If you have any question about XU212-256-TQ128-I20, please do not hesitate to contact us!

Website: <https://www.heisener.com>

E-mail: [salesdept@heisener.com](mailto:salesdept@heisener.com)