



### **ZL2008ALAFT Information**

Heisener.com

For Reference Only

Part Number ZL2008ALAFT

Manufacturer Intersil

Category Integrated Circuits (ICs)

PMIC - Voltage Regulators - DC DC Switching

Controllers

**Description** IC REG CTRLR BUCK PMBUS 36QFN

Package 36-VFQFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

## **Certified Quality**

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









# **ZL2008ALAFT Specifications**

Manufacturer Part Number	ZL2008ALAFT
Manufacturer	Intersil
Category	Integrated Circuits (ICs)
	PMIC - Voltage Regulators - DC DC Switching Controllers
Package	36-VFQFN Exposed Pad
Series	-
Output Type	Transistor Driver
Function	Step-Down
Output Configuration	Positive
Topology	Buck
Number of Outputs	1
Output Phases	1
Voltage - Supply (Vcc/Vdd)	3 V ~ 5.5 V
Frequency - Switching	200kHz ~ 1.4MHz
Duty Cycle (Max)	95%
Synchronous Rectifier	Yes
Clock Sync	Yes
Serial Interfaces	I2C, PMBus, SMBus
Control Features	Current Limit, Enable, Power Good, Soft Start
Operating Temperature	-40°C ~ 85°C (TA)
Package / Case	36-VFQFN Exposed Pad
Supplier Device Package	36-QFN (6x6)
	Report errors?

#### **ZL2008ALAFT Guarantees**



#### **Quality Guarantees**

We provide 90 days warranty. \*

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



#### **Service Guarantees**

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

## **ZL2008ALAFT Payment Methods**



















# **ZL2008ALAFT Shipping Methods**













If you have any question about ZL2008ALAFT, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com