



ZL30254LDG1 Information

Heisener.com

For Reference Only

Part Number ZL30254LDG1

Manufacturer Microchip Technology

Category Integrated Circuits (ICs)

Clock/Timing - Clock Generators, PLLs,

Frequency Synthesizers

Description IC JITTER ATTENUATOR 3I/O 32QFN

Package 32-VFQFN Exposed Pad

For the pricing/inventory/lead time, please contact

us

Website: https://www.heisener.com E-mail: salesdept@heisener.com



Request a Quote

Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.









ZL30254LDG1 Specifications

| Manufacturer Part Number | ZL30254LDG1 |
|-----------------------------|---|
| Manufacturer | Microchip Technology |
| Category | Integrated Circuits (ICs) |
| | Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers |
| Package | 32-VFQFN Exposed Pad |
| Series | - |
| Type | - |
| PLL | Yes |
| Input | CMOS, Crystal |
| Output | CML |
| Number of Circuits | 1 |
| Ratio - Input:Output | 3:2 |
| Differential - Input:Output | No/Yes |
| Frequency - Max | 156.25MHz |
| Divider/Multiplier | Yes/Yes |
| Voltage - Supply | 1.71V ~ 3.465V |
| Operating Temperature | -40°C ~ 85°C |
| Mounting Type | Surface Mount |
| Package / Case | 32-VFQFN Exposed Pad |
| Supplier Device Package | 32-QFN (5x5) |
| | Report errors? |

ZL30254LDG1 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ZL30254LDG1 Payment Methods



















ZL30254LDG1 Shipping Methods













If you have any question about ZL30254LDG1, please do not hesitate to contact us!

Website: https://www.heisener.com E-mail: salesdept@heisener.com