

ZL40240LDG1 Information


For Reference Only

Part Number [ZL40240LDG1](#)
Manufacturer Microchip Technology
Category Integrated Circuits (ICs)
[Clock/Timing - Clock Buffers, Drivers](#)
Description TEN LVCMOS OUTPUT LOW ADDITIVE J
Package 32-VFQFN Exposed Pad
 For the pricing/inventory/lead time, please contact us
 Website: <https://www.heisener.com>
 E-mail: salesdept@heisener.com


[Request a Quote](#)
Certified Quality

Heisener's commitment to quality has shaped our processes for sourcing, testing, shipping, and every step in between. This foundation underlies each component we sell.


ZL40240LDG1 Specifications

| | |
|-----------------------------|--|
| Manufacturer Part Number | ZL40240LDG1 |
| Manufacturer | Microchip Technology |
| Category | Integrated Circuits (ICs) Clock/Timing - Clock Buffers, Drivers |
| Package | 32-VFQFN Exposed Pad |
| Series | - |
| Type | Fanout Buffer (Distribution), Multiplexer |
| Number of Circuits | 1 |
| Ratio - Input:Output | 3:10 |
| Differential - Input:Output | Yes/No |
| Input | CML, HCSL, LVCMOS, LVDS, LVPECL, SSTL |
| Output | LVCMOS |
| Frequency - Max | 250MHz |
| Voltage - Supply | 1.35V ~ 3.465V |
| Operating Temperature | -40°C ~ 85°C |
| Mounting Type | Surface Mount |
| Package / Case | 32-VFQFN Exposed Pad |
| Supplier Device Package | 32-QFN (5x5) |

[Report errors?](#)

ZL40240LDG1 Guarantees



Quality Guarantees

We provide 90 days warranty. *

If the items you received were not in perfect quality, we would be responsible for your refund or replacement, but the items must be returned in their original condition.



Service Guarantees

We guarantee 100% customer satisfaction.

Our experienced sales team and tech support team back our services to satisfy all our customers.

ZL40240LDG1 Payment Methods



ZL40240LDG1 Shipping Methods



If you have any question about ZL40240LDG1, please do not hesitate to contact us!

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